Program: BE Electronics and Telecommunication Engineering

Curriculum Scheme: Revised 2016

Examination: Third Year Semester VI

Course Code: ECCDLO6021 Course Name: Digital VLSI Design

Time: 1 hour Max. Marks: 50

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| **Question** | **Correct Option** |
| Q1. | **B** |
| Q2. | C |
| Q3. | D |
| Q4. | C |
| Q5. | C |
| Q6. | A |
| Q7. | A |
| Q8. | C |
| Q9. | B |
| Q10. | B |
| Q11. | C |
| Q12. | A |
| Q13. | B |
| Q14. | A |
| Q15. | A |
| Q16. | C |
| Q17. | D |
| Q18. | A |
| Q19. | B |
| Q20. | B |
| Q21. | D |
| Q22. | A |
| Q23. | C |
| Q24. | B |
| Q25. | A |